



100

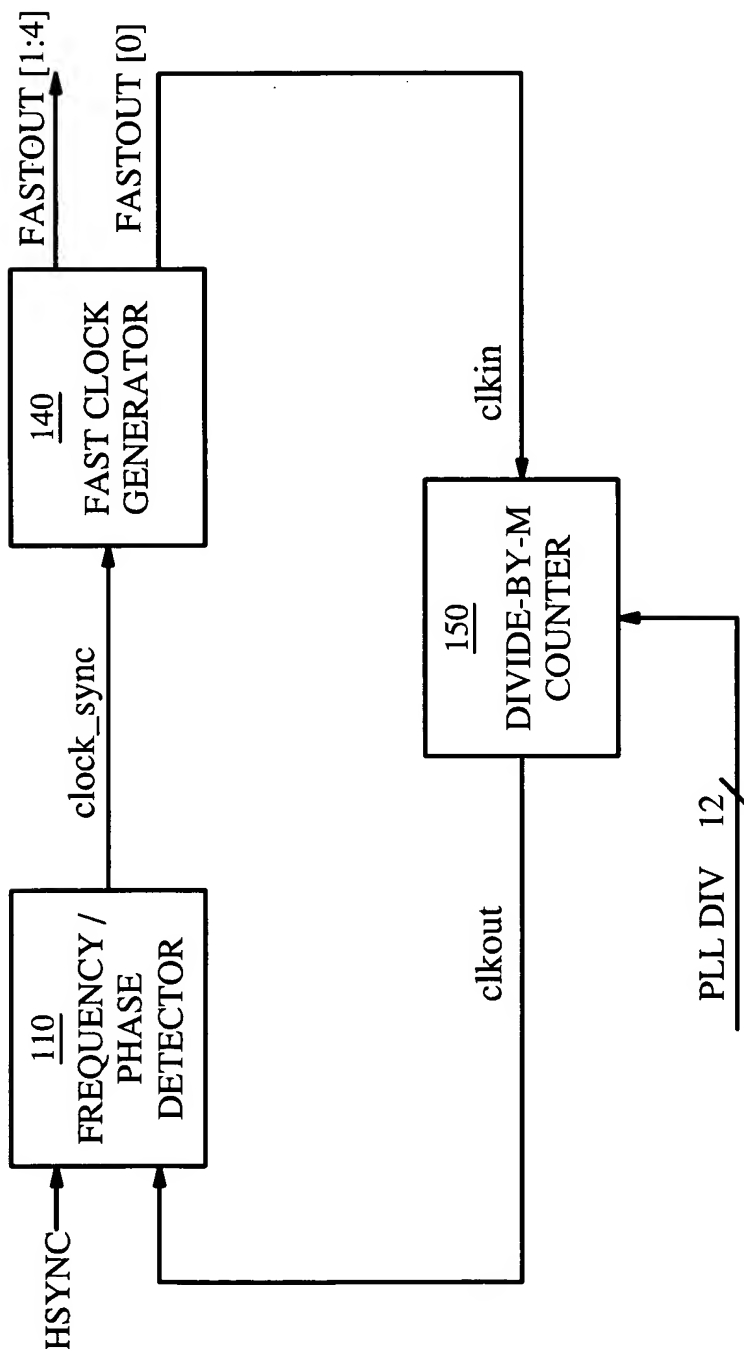


Figure 1

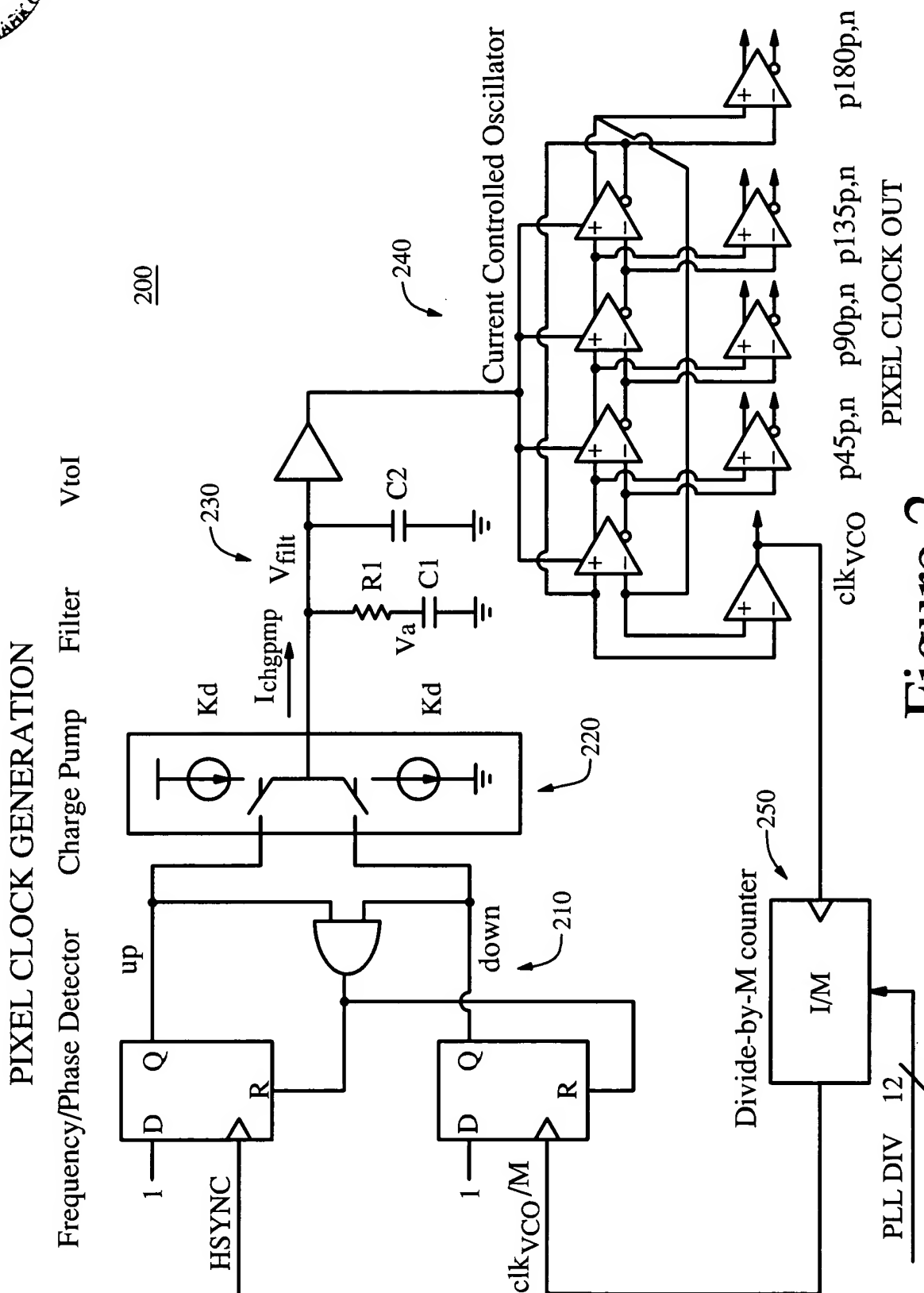
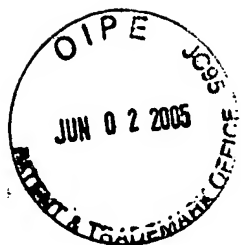


Figure 2



3 / 11

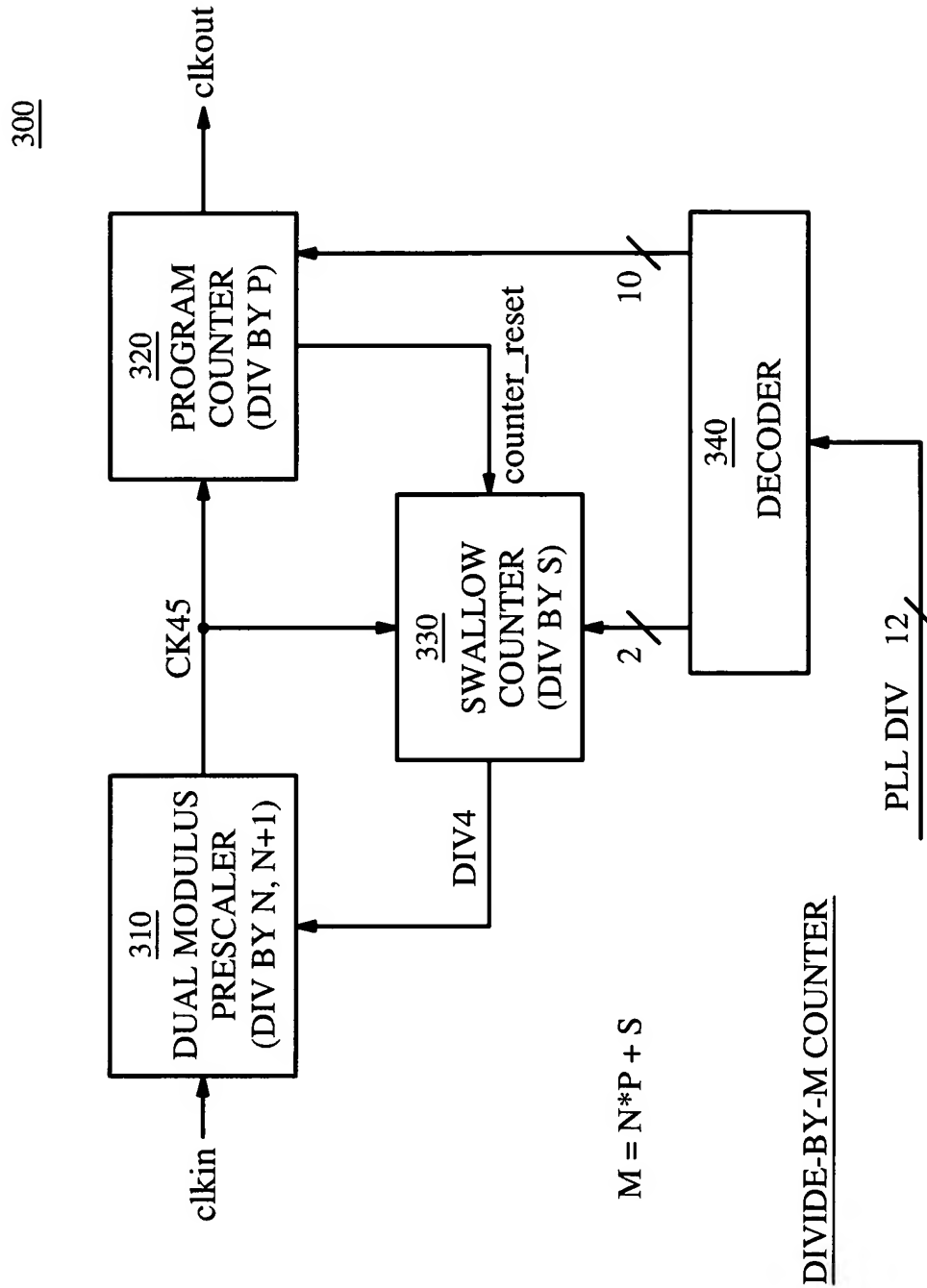


Figure 3

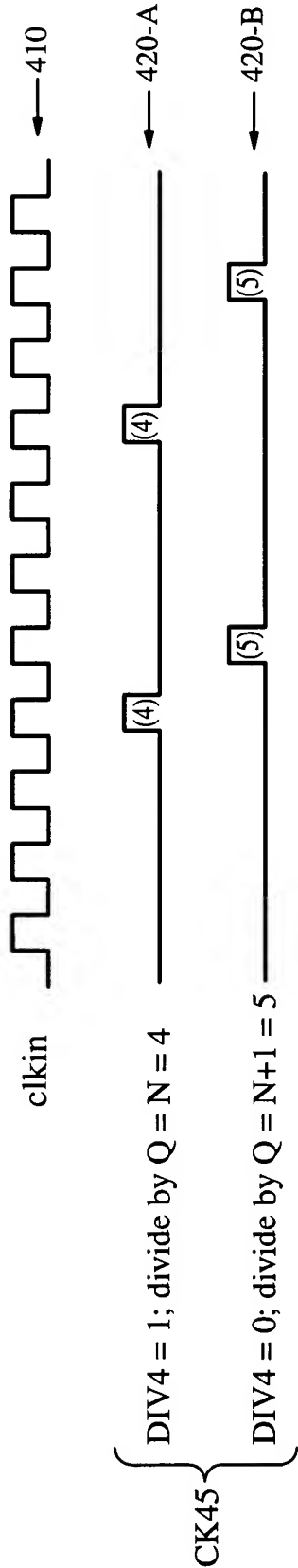


Figure 4A

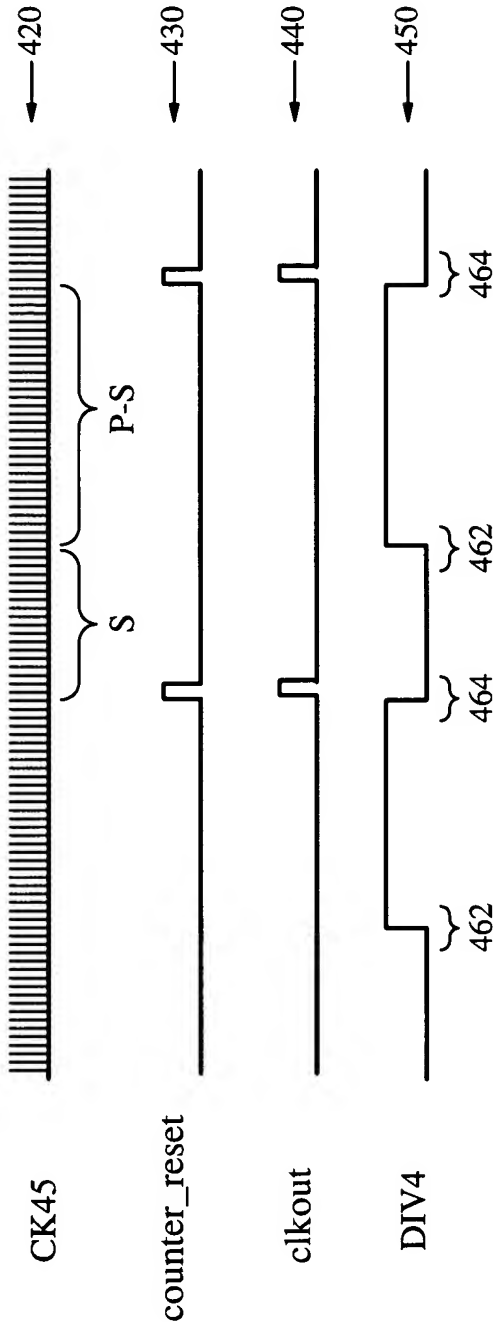


Figure 4B



500

M = N * P + S

FOR N=4

M	DIV ← M-1 (12 bit)	P ← DIV[11:2] (10 bit)	S ← DIV[1:0] (2 bit)
(12 bit)		(10 bit)	(2 bit)
(values)		(values)	(values)
17-20	...	4	0-3
21-24		5	0-3
25-28		6	0-3
29-32		7	0-3
33-36		8	0-3
37-40		9	0-3
...		...	
1009-1013		252	0-3
...		...	
2033-2036		508	0-3
...		...	
3072-3076		768	0-3
...		...	
4093-4096		1023	0-3

Figure 5

6 / 11

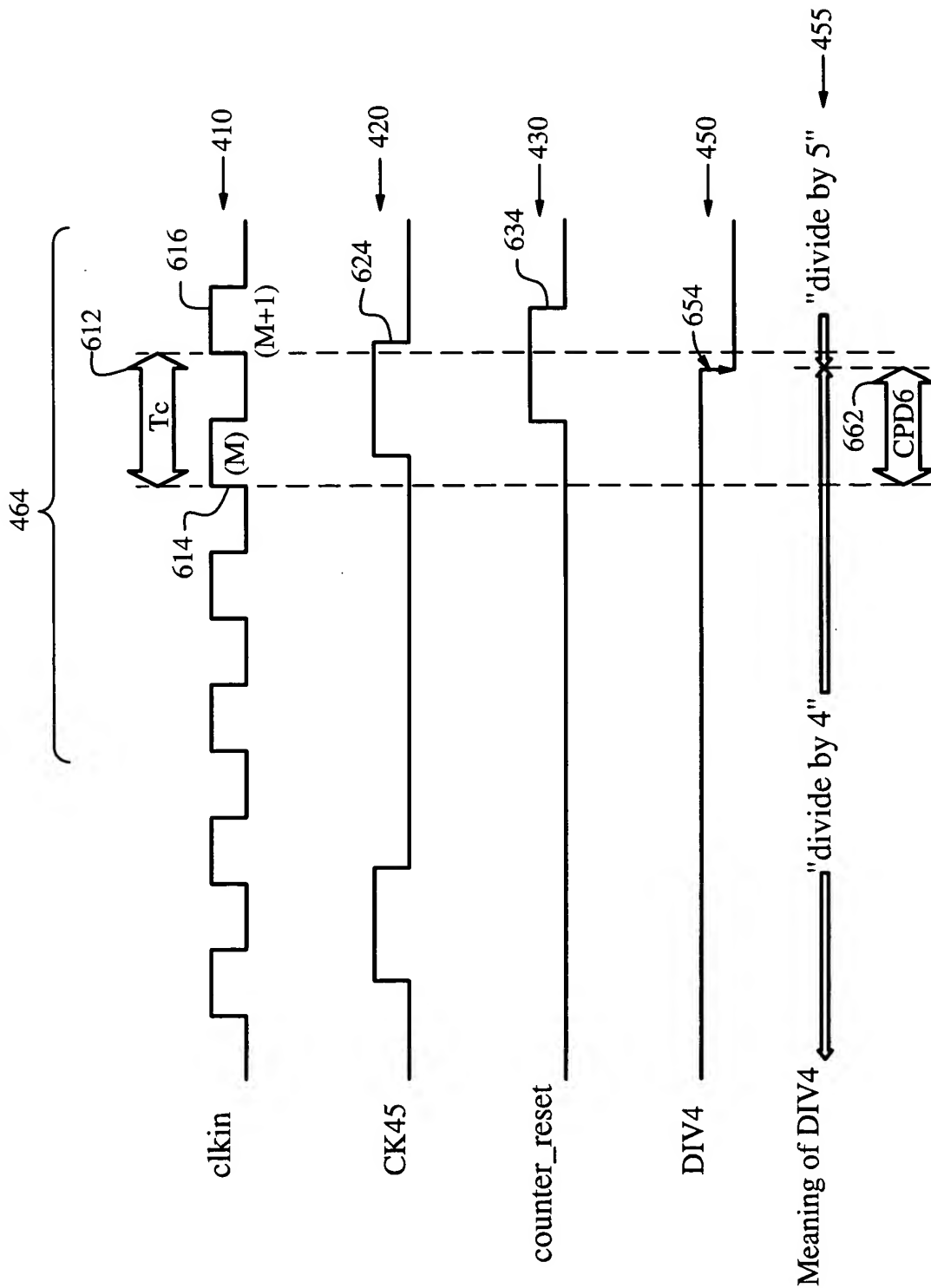


Figure 6





7 / 11

700

N = 4

VARIABLE TYPE	SYMBOL	START (AT POR)	NEXT ASSIGNED VALUE
INTERNAL	D2	1	NEXT_D2 = DIV4 OR D0
INTERNAL	D1	1	NEXT_D1 = ~ (D2 AND D0)
INTERNAL	D0	1	NEXT_D0 = D1
OUTPUT	CK45	1	NEXT_CL45 = D2 AND D1 AND D0

XX = DON'T CARE

PRESCALER LOGIC

Figure 7



8 / 11

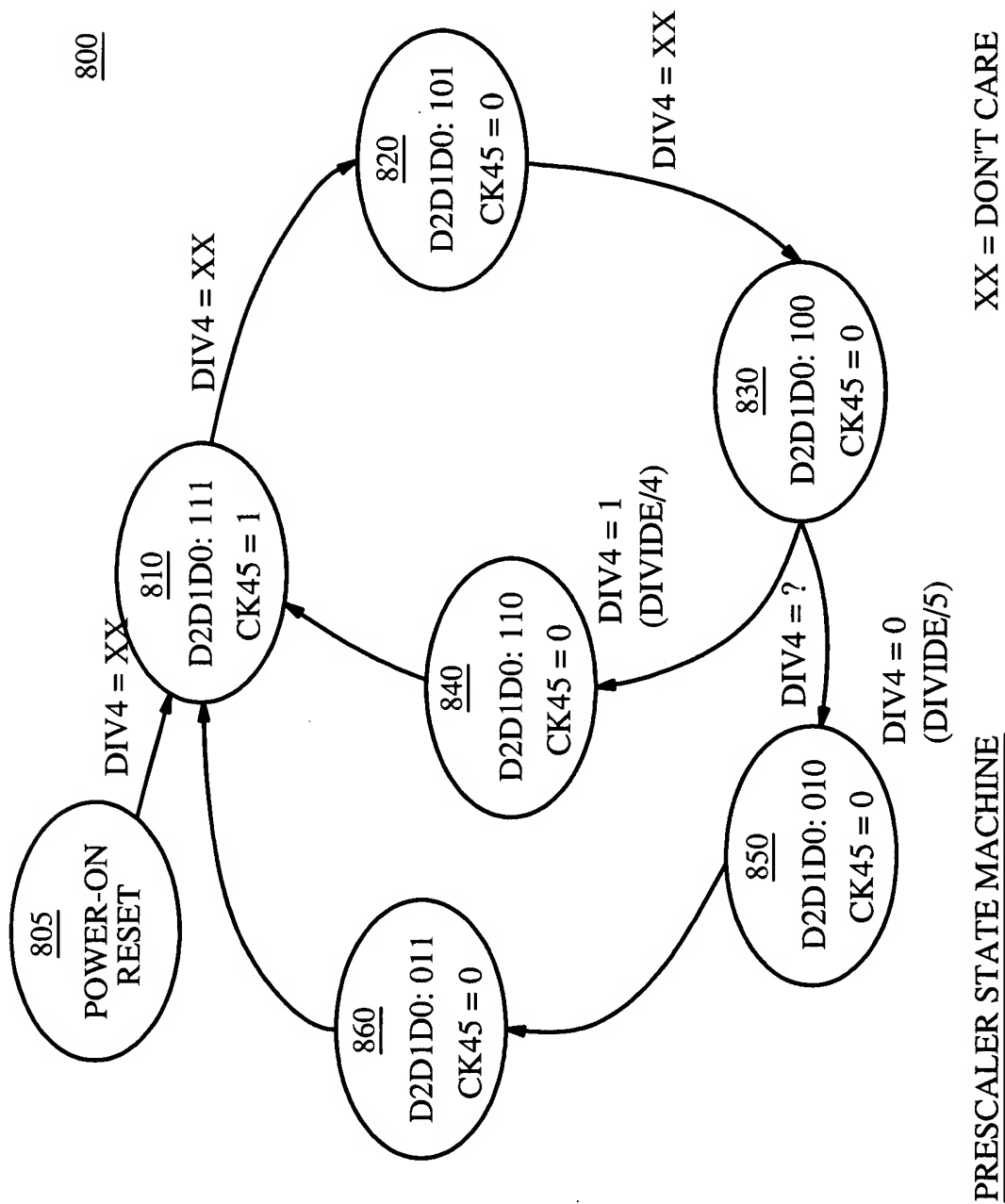


Figure 8



9 / 11

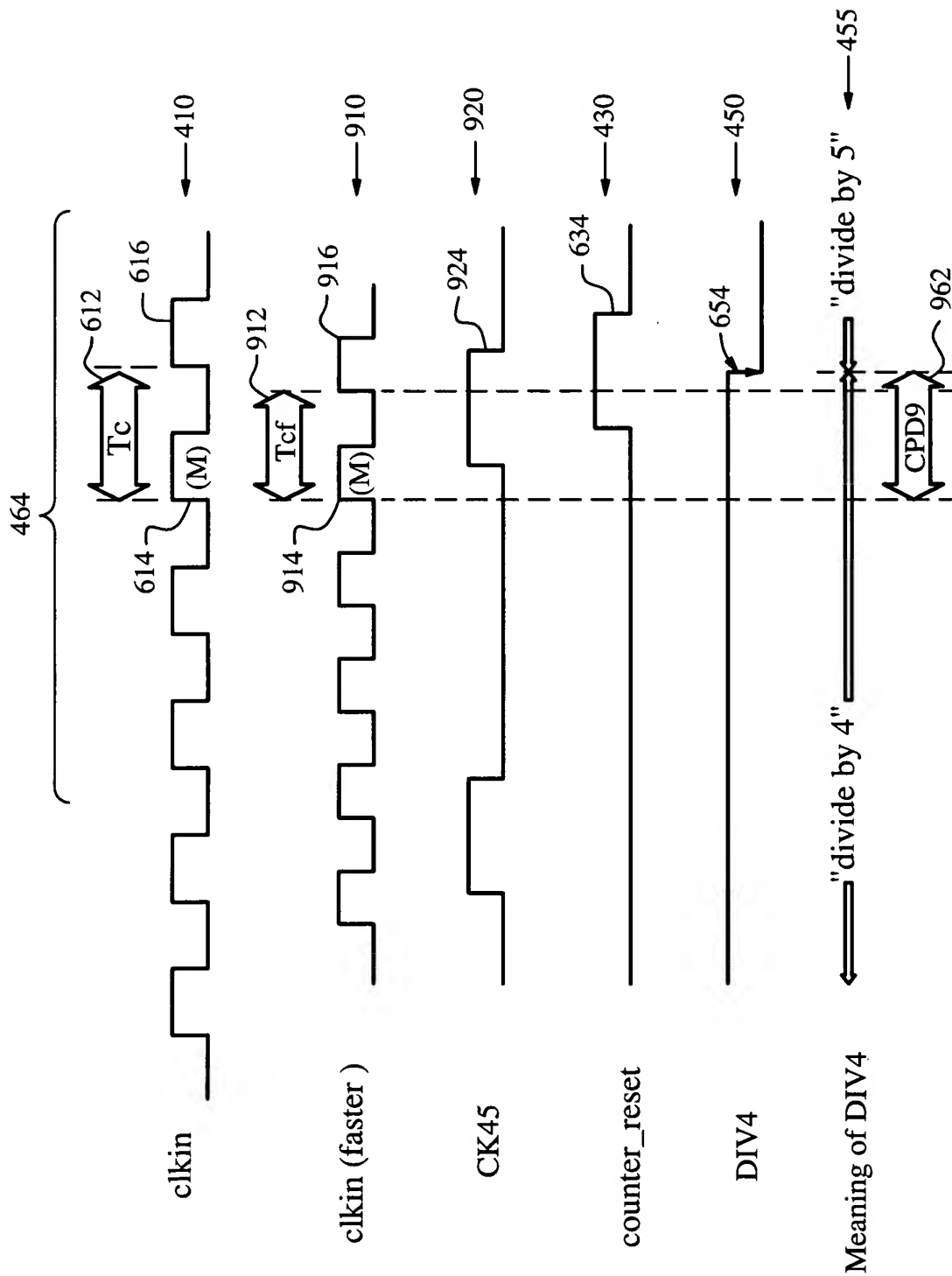


Figure 9

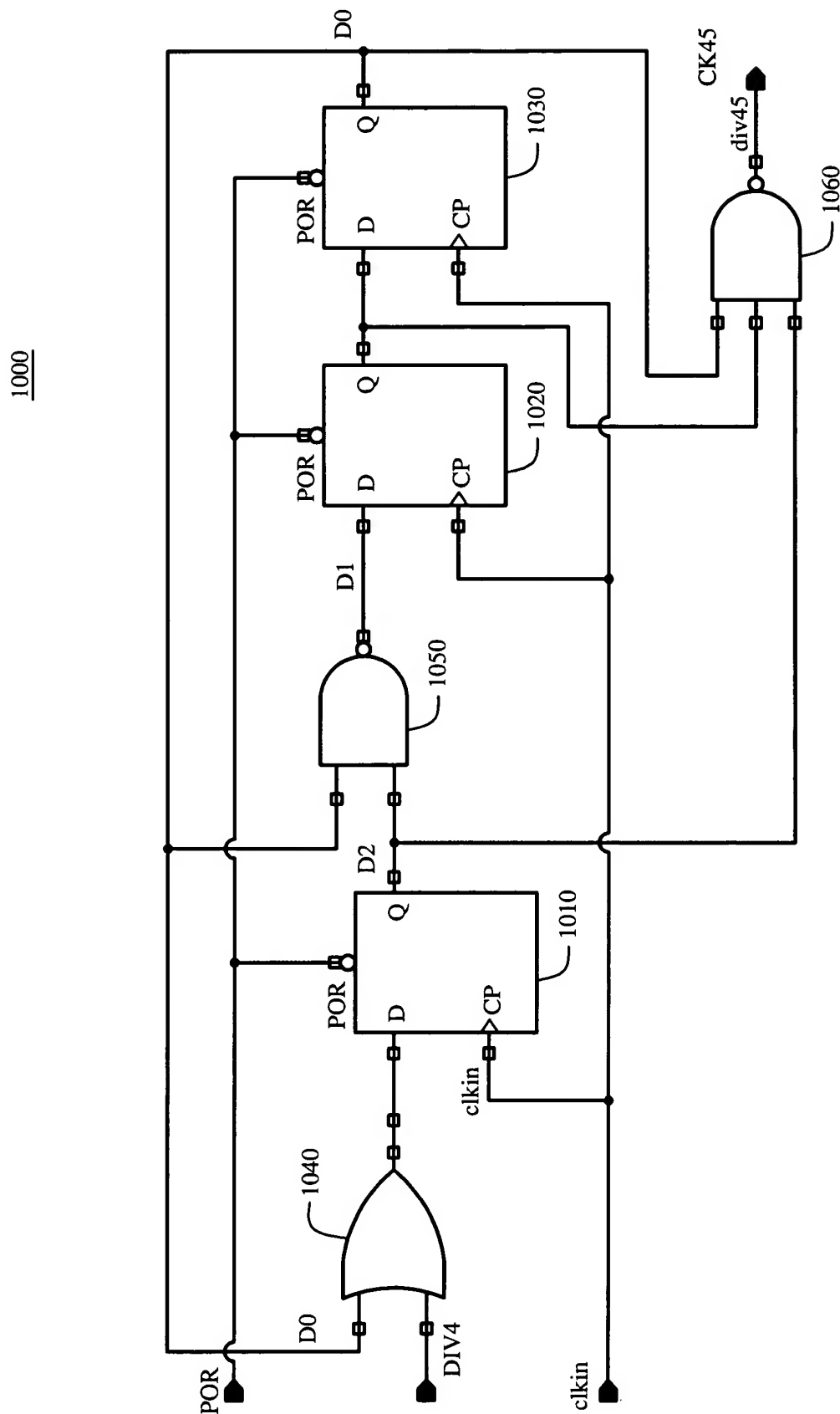
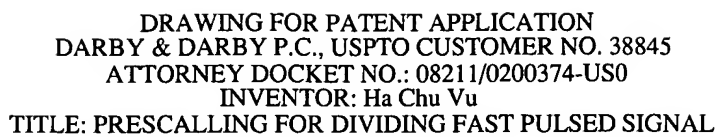


Figure 10



11 / 11

1100

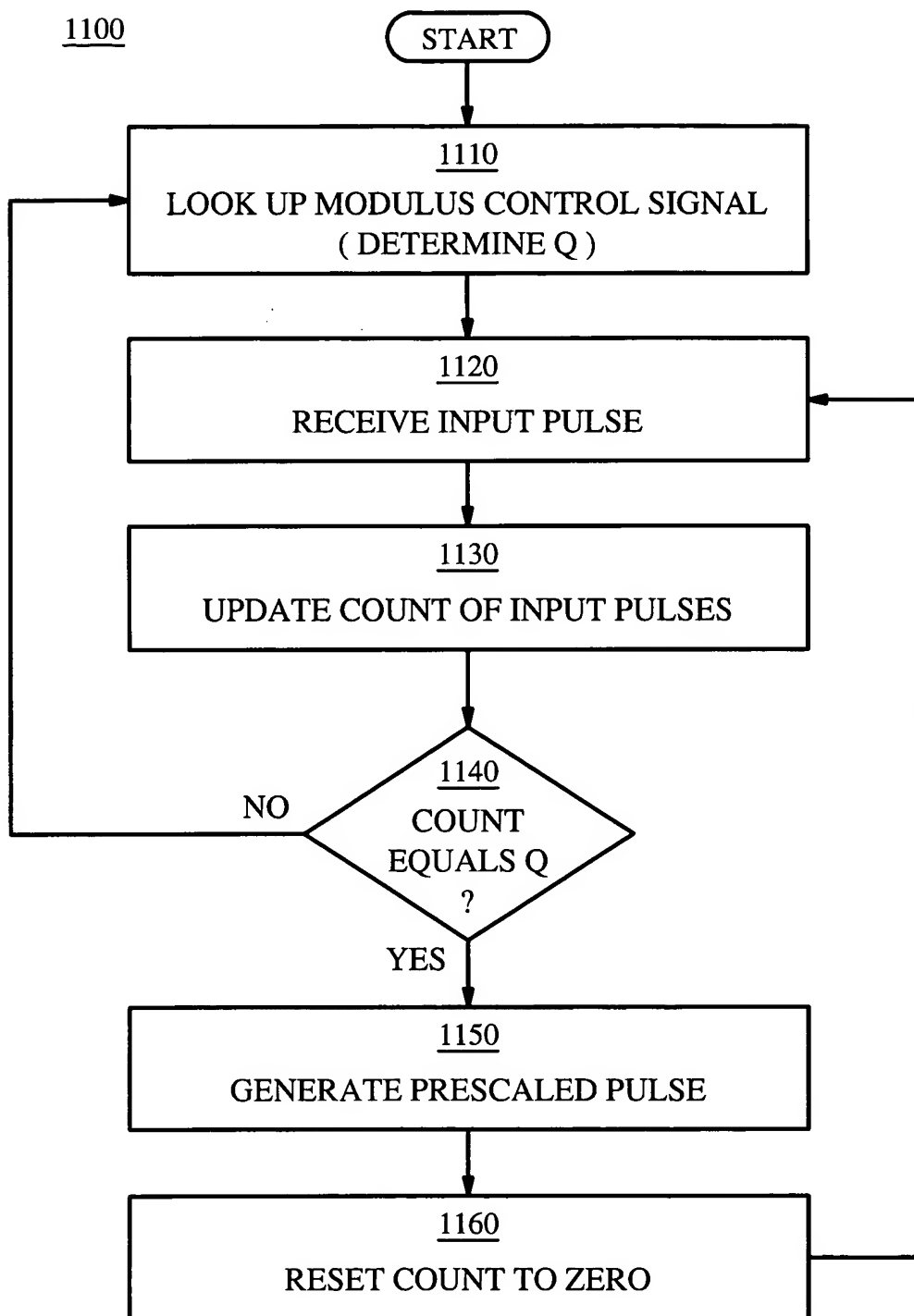


Figure 11